

AUTOMOTIVE CURRENT TRANSDUCER HAH1DR 400-S









Introduction

The HAH1DR family is for the electronic measurement of DC, AC or pulsed currents in high power automotive applications with galvanic isolation between the primary circuit (high power) and the secondary circuit (electronic circuit).

The HAH1DR family gives you the choice of having different current measuring ranges in the same housing (from \pm 200 A up to \pm 900 A).

Features

- · Open Loop transducer using the Hall effect
- Unipolar + 5 V DC power supply
- Primary current measuring range up to ± 400 A
- Maximum rms primary current limited by the busbar, the magnetic core or the ASIC temperature T° < + 150°C
- Operating temperature range: 40°C < T° < + 125°C
- Output voltage: full ratio-metric (in gain and offset)
- · Compact design.

Advantages

- Excellent accuracy
- Very good linearity
- Very low thermal offset drift
- Very low thermal gain drift
- Wide frequency bandwidth
- No insertion losses.

Automotive applications

- Battery monitoring
- Starter Generators
- Inverters
- HEV application
- EV application.

Principle of HAH1DR Family

The open loop transducers use an Hall effect integrated circuit.

The magnetic flux density B, contributing to the rise of the Hall voltage, is generated by the primary current $\mathbf{I}_{\rm P}$ to be measured.

The current to be measured I_p is supplied by a current source i.e. battery or generator (Fig. 1).

Within the linear region of the hysteresis cycle, B is proportional to:

B
$$(I_p)$$
 = constant (a) $\times I_p$

The Hall voltage is thus expressed by:

$$\mathbf{V}_{H} = (\mathbf{R}_{H}/\mathbf{d}) \times \mathbf{I} \times \mathbf{constant} (\mathbf{a}) \times \mathbf{I}_{P}$$

Except for $\mathbf{I}_{\mathbf{p}^{\mathrm{s}}}$ all terms of this equation are constant. Therefore:

$$V_{H}$$
 = constant (b) x I_{P}

The measurement signal $\mathbf{V}_{_{\mathrm{H}}}$ amplified to supply the user output voltage or current.

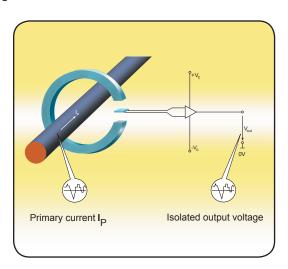


Fig. 1: Principle of the open loop transducer



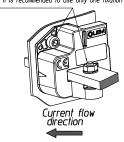
HAH1DR 400-S

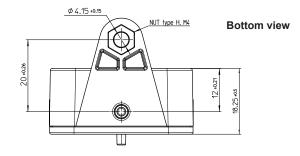
Dimensions HAH1DR family (in mm. 1mm = 0.0394 inch)

MOUNTING RECOMMENDATION (2 mounting options)

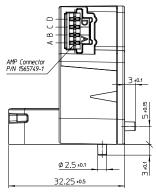
- Use screw 04 mm Tightening torque : 2.5 N.m ±5% (1.85 lbf.ft) Recommended torque 2.2 N.m ±5% Use 'Grower' spring washers

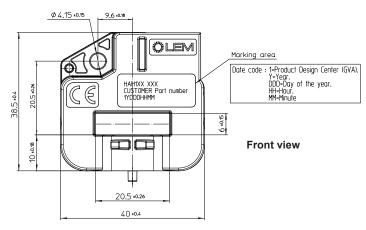
- It is recommended to use only one fixation point at the same time.





Right view





Bill of materials

 Plastic case PBT GF 30 Magnetic core Iron silicon alloy Pins Brass tin platted Weight 39 g

Remarks

 $\frac{V_c}{2}$ when I_p flows in the direction of the arrow.

System architecture (example)

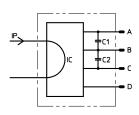
>10 k Ω optional resistor for signal line diagnostic

V _{OUT}	Diagnosis
Open circuit	V _{IN} = V _C
Short GND	V _{IN} = OV

< 100 nF EMC protection

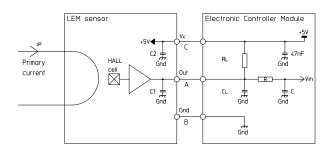
RC Low pass filter EMC protection (optional)

System artitecture



	Components list	DR version	BV version
IC	Hall sensor ASIC		
C1	Decoupling capacitor	47nF	10nF
C2	Decoupling capacitor	4.7⊓F	100nF

	Pin out					
Α	Vout					
В	Ground					
С	Vc (5V)					
D	Ground					





HAH1DR 400-S

Absolute maximum ratings

Downston	Cumph al	Unit	Specification			0 1111
Parameter	Symbol		Min	Тур	Max	Conditions
Electrical Data						
Max primary current peak	I _{Pmax}	Α			1)	
Supply continuous over voltage		V			7	Not operating
Reverse voltage 2)	→ v _c		-0.5			1 min @ T _A = 25°C
Output over voltage (continuous)	V _{OUT}	V	-0.5		V _c + 0.5	
Continuous output current	I _{OUT}	mA	-10		10	
Output short-circuit duration	T _c	min			2	
Rms voltage for AC isolation test	V _d	kV			2	50 Hz, 1 min
Isolation resistance	R _{IS}	MΩ	500			500 V - ISO 16750-2
Electrostatic discharge voltage	V _{ESD}	kV			2	JESD22-A114-B
Ambient storage temperature	T _s	°C	-40		125	

Operating characteristics

Parameter	Symbol	Unit	Specification			Conditions
Faranietei			Min	Тур	Max	Conditions
Electrical Data						
Primary current	I _p	Α	-400		400	
Calibration current	I _{CAL}	Α	-400		400	@ T _A = 25°C
Supply voltage	$\mathbf{V}_{_{\mathrm{C}}}$	V	4.75	5.00	5.25	
Output voltage (Analog) 3)	V _{OUT}	V	V _{OUT} = (V _c /5) × (2.5 -	+ G × I _P)	@ v _c
Sensitivity 3)	G	mV/A		5		@ V _C = 5 V
Current consumption	I _c	mA		15	20	@ V _C = 5 V, @ - 40°C < T _A < 125°C
Load resistance	$R_{\scriptscriptstyle L}$	ΚΩ	10			
Output internal resistance	R _{OUT}	Ω			10	DC to 1 kHz
Capacitive loading	C _L	nF	1		100	
Ambient operating temperature	T _A	°C	-40		125	Connector limited 105°C
Output drift versus power supply	V _{OUT PS}	%		0.5		
Performance Data						
Sensitivity error	$\epsilon_{_{ m G}}$	%	-1.0	± 0.5	1.0	@ T _A = 25°C @ I = I _P
Electrical offset current	I _{OE}			± 0.8		@ T _A = 25°C
Magnetic offset current	I _{OM}	Α		± 1.0		@ T _A = 25°C, after ± I _P
Global offset current	I _o		-2.2		2.2	@ T _A = 25°C
Average temperature coefficient of $\mathbf{V}_{\scriptscriptstyle{\mathrm{OE}}}$	TCV _{OE AV}	mV/°C	-0.08	± 0.03	0.08	@ - 40°C < T° < 125°C
Average temperature coefficient of G	TCG _{AV}	%/°C	-0.035	± 0.02	0.035	@ - 40°C < T° < 125°C
Linearity error	$\epsilon_{\scriptscriptstyle \! L}$	%	-1		1	$\textcircled{@} V_{C} = 5 \lor \textcircled{@}, T_{A} = 25^{\circ}C, \textcircled{@} I = I_{P}$
Response time to 90 % of I _{PN} step	t,	μs		6	10	@ di/dt = 100 A/µs
Frequency bandwidth 4)	BW	kHz		30		@ -3 dB
Output clamping voltage min	V _{sz}	V			0.1	@ V _C = 5 V, T _A = 25°C
Output clamping voltage max	V _{sz}	V	4.9			@ V _C = 5 V, T _A = 25°C
Output voltage noise peak-peak	V _{no pp}	mV			20	DC to 1MHz

Notes:

- ¹⁾ Busbar temperature must be below 150°C
- ²⁾ Transducer not protected against reverse polarity.

$$I_{P} = \left(V_{\text{out}} - \frac{V_{c}}{2}\right) \times \frac{1}{G} \times \frac{5}{V_{c}} \quad \text{with G in (V/A)}$$

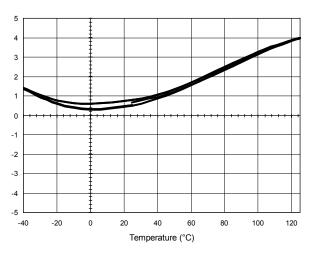
 $^{^{3)}}$ The output voltage \mathbf{V}_{OUT} is fully ratiometric. The offset and gain are dependent on the supply voltage \mathbf{V}_{C} relative to the following formula:

⁴⁾ Tested only with small signal only to avoid excessive heating of the magnetic core.

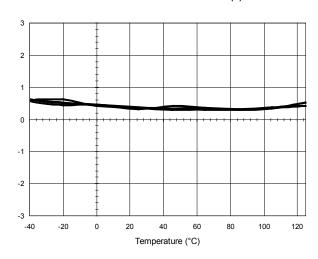


HAH1DR 400-S

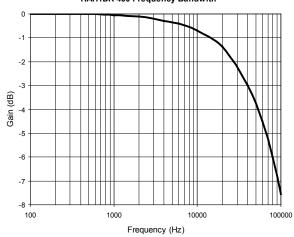




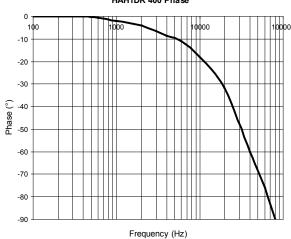
HAH1DR 400 Electrical offset Error (A)



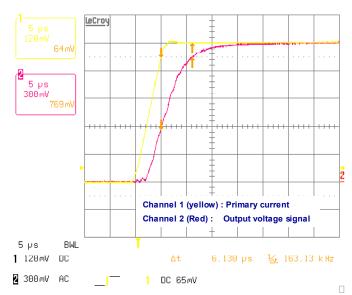
HAH1DR 400 Frequency Bandwith



HAH1DR 400 Phase



Typical response time at 100 A/ μ s





HAH1DR 400-S PERFORMANCES PARAMETERS DEFINITIONS

Output noise voltage:

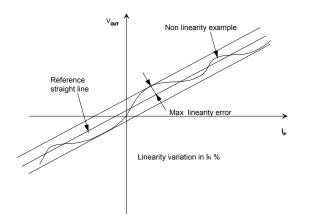
The output voltage noise is the result of the noise floor of the Hall elements and the linear I_c amplifier gain.

Magnetic offset:

The magnetic offset is the consequence of an over-current on Offset with temperature: the primary side. It's defined after an excursion of $I_{P,max}$.

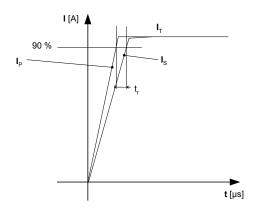
The maximum positive or negative discrepancy with a reference straight line $V_{OUT} = f(I_P)$.

Unit: linearity (%) expressed with full scale of I_{P max}. Linearity is measured on cycle + I_p , O, - I_p , O, + I_p without magnetic offset (average values used).



Response time (delay time) t.:

The time between the primary current signal and the output signal reach at 90 % of its final value.



Typical:

Theorical value or usual accuracy recorded during the production.

Sensitivity:

The Transducer's sensitivity **G** is the slope of the straight line $V_{out} = f(I_p)$, it must establish the relation:

 $V_{out}(I_P) = V_C/5$ (**G** x $I_P + 2.5$) (*)(*) For all symetrics transducers.

The error of the offset in the operating temperature is the variation of the offset in the temperature considered with the initial offset

The offset variation I_{OT} is a maximum variation the offset in the temperature range:

 $I_{OT} = I_{OF} \max - I_{OF} \min$

The Offset drift \mathbf{TCI}_{OEAV} is the \mathbf{I}_{OT} value divided by the temperature range.

Sensitivity with temperature:

The error of the sensitivity in the operating temperature is the relative variation of sensitivity with the temperature considered with the initial offset at 25°C.

The sensitivity variation $\mathbf{G}_{\scriptscriptstyle T}$ is the maximum variation (in ppm or %) of the sensitivity in the temperature range:

 \mathbf{G}_{T} = (Sensitivity max - Sensitivity min) / Sensitivity at 25°C.

The sensitivity drift TCG_{AV} is the G_T value divided by the temperature range.

Offset voltage @ $I_p = 0$ A:

Is the output voltage when the primary current is null. The ideal value of V_o is $V_c/2$ at $V_c = 5$ V. So, the difference of $V_o - V_c/2$ is called the total offset voltage error. This offset error can be attributed to the electrical offset (due to the resolution of the ASIC quiescent voltage trimming), the magnetic offset, the thermal drift and the thermal hysteresis.

Environmental test specifications

Name	Standard	Conditions		
Damp heat, steady state	JESD22-A101	85°C - 85°C / 1000h		
Isolation resistance	ISO 16750-2 § 4.10	500 V/1min		
Temperature humidity cycle test	ISO 16750-4	-10 + 85°C 10 days		
Isolation test	IEC 60664-1	2 kV/50 Hz/1min		
Mechanical tests				
Vibration test (random)	IEC 60068-2-64 ISO 16750-3 & 4.1.2.5 (2007)	20 2000 Hz Random rms (11g rms) 8h/axis		
Terminal strength test	According to LEM			
Thermal shocks	IEC 60068-214 Na	-40 + 125°C 300 cycles		
Free fall	ISO 16750-3 § 4.3	1m concrete ground		
EMC Test				
Radiated electronagnetic immunity	Directive 2004/104/CE ISO 11452-2	30 V/m 20-2000 MHz		
Bulk current injection immunity	Directive 2004/104/CE ISO 11452-4	1-400 MHz - 60 mA		
Radiated radio frequency electromagnetic field immunity	IEC 61000-4-3	80000 MHz-10V/m		
Electrostatic discharge immunity test	IEC 61000-4-2	Air discharge=2 kV		